



J12 CAN BE USED TO CHOOSE WHICH ADDRESS THIS BOARD WILL RESPOND TO. I.E. SET A17-19 TO THE ADDRESS SENT OUT BY THE 68332 WHEN ACCESSING THESE PERIPHERALS.

Each 8 bit bidirectional port is accompanied by a matching pair of power and ground pins to facilitate I/O connections. Additionally, JP19 can be switched between Vcc and V_motor.

J14 can be used to select which chip select this board will respond to.

WHEN A4 IS LOW THE PIA IS ACCESSED, WHEN HIGH, THE AtD IS ACCESSED

Title (Title)		
Size B	Document Number (Doc)	Rev (Rev Code)
Date:	Wednesday, July 19, 2000	Sheet 2 of 3